

Electronic Acknowledgement Receipt

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Patent Number:	6871298
Confirmation Number:	4116
Petition Issued Date:	June 25,2009
Title of Invention:	METHOD AND APPARATUS THAT SIMULATES THE EXECUTION OF PARALLED INSTRUCTIONS IN PROCESSOR FUNCTIONAL VERIFACATION TESTING
First Named Inventor/Applicant Name:	Becky Cavanaugh
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Filer Authorized By:	
Attorney Docket Number:	075963.0104
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Petition automatically granted by EFS	sb0066e_fill.pdf	968091	no	3
			e87ca3ed58bb20f87554624d3fa137bde9c14789		

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	32249	no	2
			8e631be0c171935c473dda5bc10650651bc0767c		

Warnings:

Information:

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.